JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Results for   M.Tech (R16 / R13) I Semester Regular / Supplementary Examinations , JANUARY- 2019 .

College: GAYATHRI VIDYA PARISHAD COLL OF ENGG FOR WOMEN, VISAKHAPATNA:JG

Discrepancy pertaining to these results are to be submitted on or before 10-05-2019 with following documents at CE(PG) Office,JNTUK,Kakinada

Htno	Subcode	Subname	Internal	External	credits
16JG1D7704	I6803	CMOS ANALOG IC DESIGN	23	-1	0
16JG1D7704	I6804	HARDWARE SOFTWARE CO-DESIGN ELECTIVE 2	14	-1	0
17JG1D7705	I6801	DIGITAL SYSTEM DESIGN	21	-1	0
17JG1D7705	I6803	CMOS ANALOG IC DESIGN	24	18	0
17JG1D7705	I6804	HARDWARE SOFTWARE CO-DESIGN ELECTIVE 2	19	-1	0
17JG1D7705	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	27	-1	0
17JG1D7706	I6803	CMOS ANALOG IC DESIGN	35	11	0
17JG1D7706	I6804	HARDWARE SOFTWARE CO-DESIGN ELECTIVE 2	20	31	1
18JG1D7701	I6801	DIGITAL SYSTEM DESIGN	27	33	1
18JG1D7701	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	39	35	1
18JG1D7701	I6803	CMOS ANALOG IC DESIGN	30	28	1
18JG1D7701	I6804	HARDWARE SOFTWARE CO-DESIGN ELECTIVE 2	24	32	1
18JG1D7701	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	31	34	1
18JG1D7701	I6810	SYSTEM ON CHIP DESIGN ELECTIVE 2	35	26	1
18JG1D7701	I6812	VLSI LABORATORY	38	57	1
18JG1D7702	I6801	DIGITAL SYSTEM DESIGN	26	29	1
18JG1D7702	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	38	35	1
18JG1D7702	I6803	CMOS ANALOG IC DESIGN	30	25	1
18JG1D7702	I6804	HARDWARE SOFTWARE CO-DESIGN ELECTIVE 2	24	26	1
18JG1D7702	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	34	33	1
18JG1D7702	I6810	SYSTEM ON CHIP DESIGN ELECTIVE 2	29	24	1
18JG1D7702	I6812	VLSI LABORATORY	37	54	1
18JG1D7703	I6801	DIGITAL SYSTEM DESIGN	22	28	1
18JG1D7703	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	37	33	1
18JG1D7703	I6803	CMOS ANALOG IC DESIGN	32	29	1
18JG1D7703	I6804	HARDWARE SOFTWARE CO-DESIGN ELECTIVE 2	30	38	1
18JG1D7703	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	33	42	1
18JG1D7703	I6810	SYSTEM ON CHIP DESIGN ELECTIVE 2	35	28	1
18JG1D7703	I6812	VLSI LABORATORY	37	55	1
18JG1D7704	I6801	DIGITAL SYSTEM DESIGN	21	33	1
18JG1D7704	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	35	30	1
18JG1D7704	I6803	CMOS ANALOG IC DESIGN	23	27	1
18JG1D7704	I6804	HARDWARE SOFTWARE CO-DESIGN ELECTIVE 2	19	37	1
18JG1D7704	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	28	46	1
18JG1D7704	I6810	SYSTEM ON CHIP DESIGN ELECTIVE 2	33	25	1
18JG1D7704	I6812	VLSI LABORATORY	35	50	1
18JG1D7705	I6801	DIGITAL SYSTEM DESIGN	10	27	0
18JG1D7705	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	27	18	0
18JG1D7705	I6803	CMOS ANALOG IC DESIGN	19	17	0
18JG1D7705	I6804	HARDWARE SOFTWARE CO-DESIGN ELECTIVE 2	14	29	0
18JG1D7705	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	13	25	0

Htno	Subcode	Subname	Internal	External	credits
18JG1D7705	I6810	SYSTEM ON CHIP DESIGN ELECTIVE 2	21	19	0
18JG1D7705	I6812	VLSI LABORATORY	34	45	1

**Note:1)For Recounting/Revaluation/Challenge By Revaluation Apply through Online(www.jntukresults.edu.in))

NOTE:2 [Last Date for Apply Recounting/Revaluation/Challenge By Revaluation: **17-05-2019]

**NOTE:3 [Please inform to the students to enter these subject codes for applying Recounting/Revaluation/Challenge By Revaluation]

**NOTE:
[-1 in the filed of externals indicates student absent for the respective subject.

-2 in the filed of externals indicates student result is withheld for the respective subject.

-3 in the filed of externals indicates Malpractice for the respective subject.]

Date:03-05-2019

N. Mohan Rao

Controller of Examinations